ACKNOWLEDGEMENT RECEIPT

Electronic Version 1.1

Stylesheet Version v1.1.1

Title of Invention	Avoiding Excessive Cross-terminal Voltages of Low Voltage Transistors Due to Undesirable Supply-Sequencing in Environments With Higher Supply Voltages

Submission Type : Utility Patent Filing

Application Number:

10/711532

EFS ID: 69099

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First Named Applicant: Bhupendra SHARMA

Attorney Docket Number: TI-38242

Timestamp: 2004-09-24 01:29:22 EDT

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us-fee-sheet	us-fee-sheet.dtd	11069	2004-09-24
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application-body	Image5.tif	821167	2004-09-24

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application-body	mmlextra.ent	7901	2004-09-24
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claims-pdf	ABXspectxn108-clms.pdf	30373	2004-09-24
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